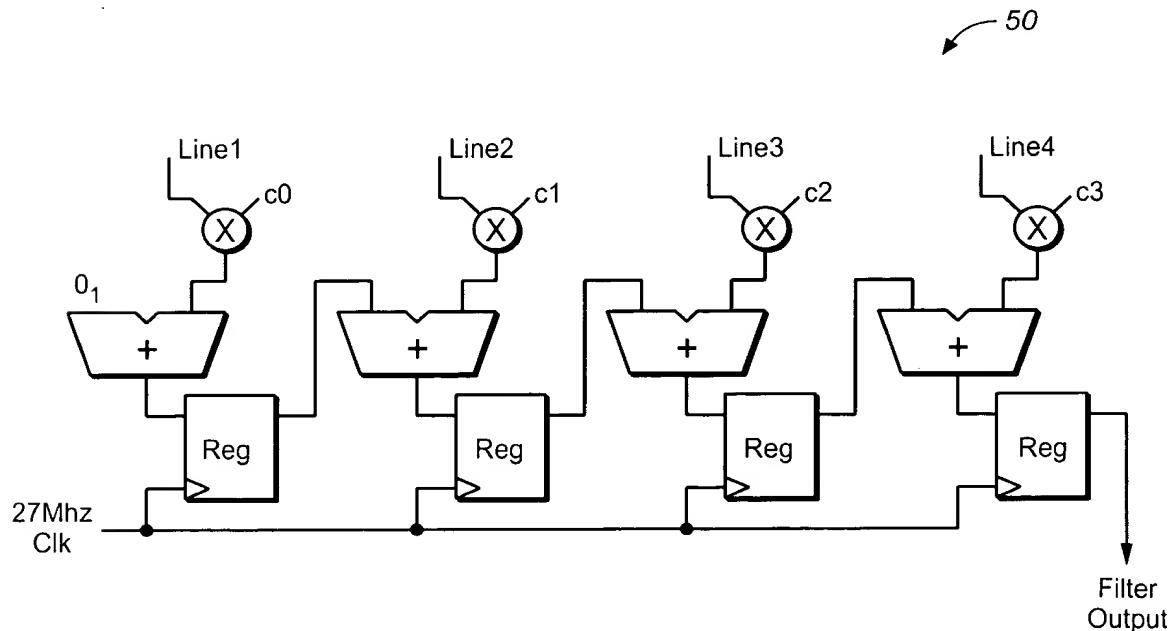
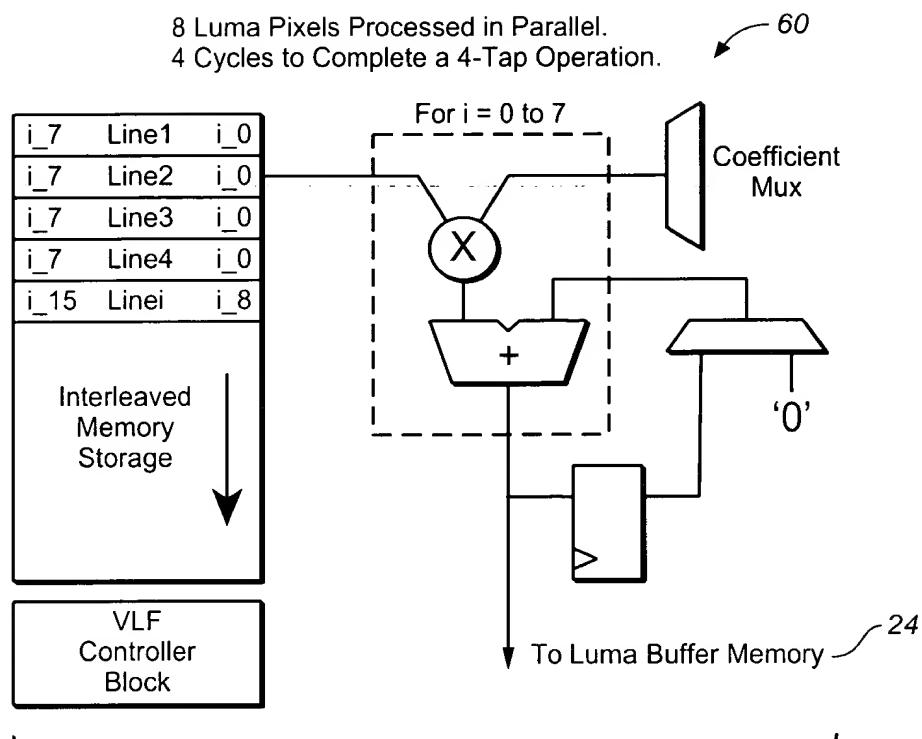
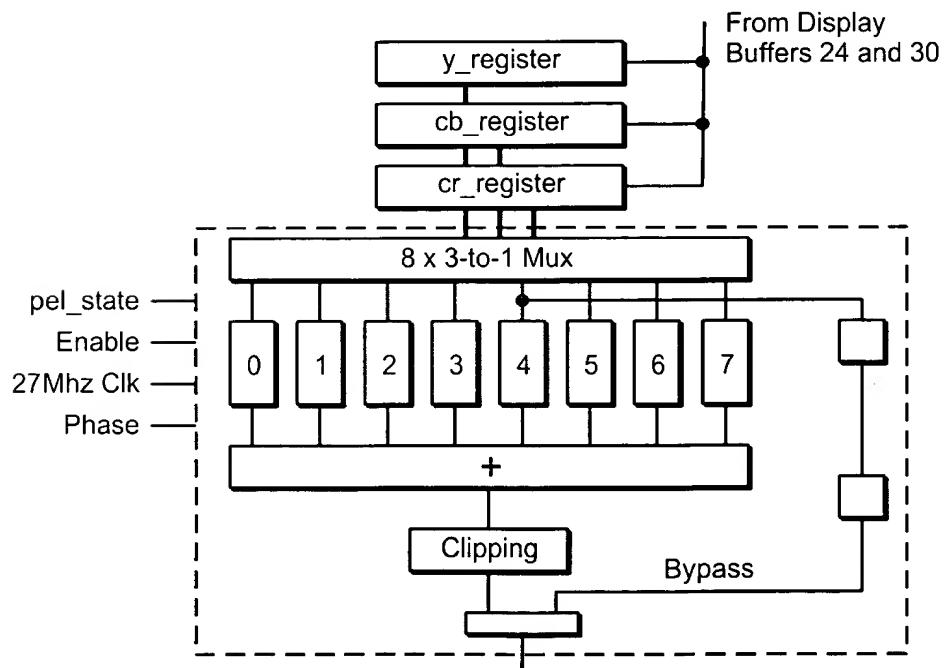
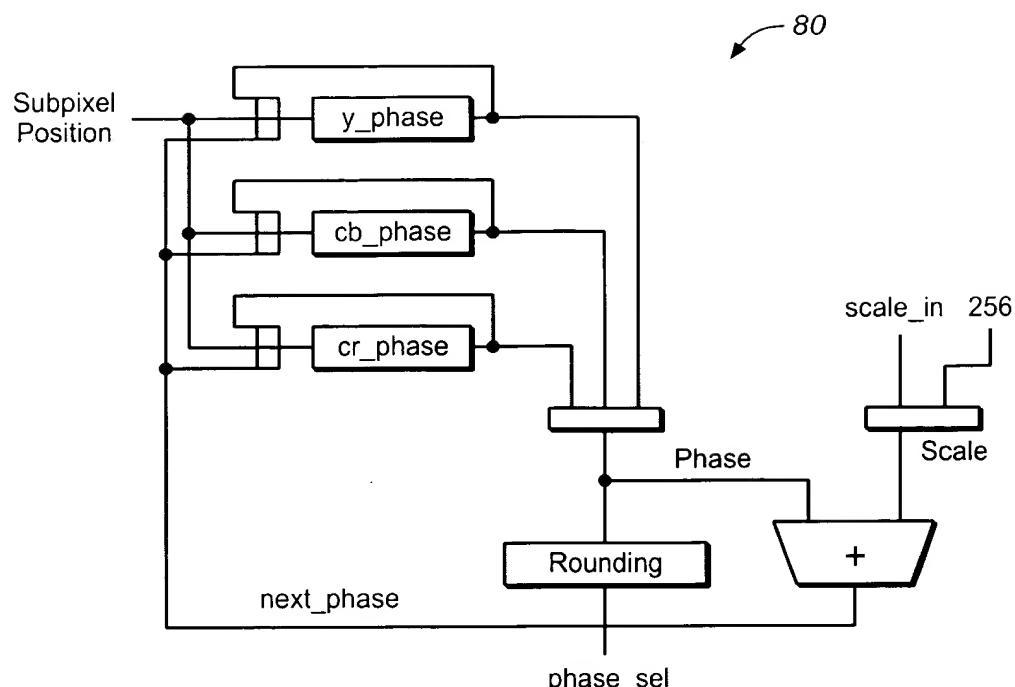
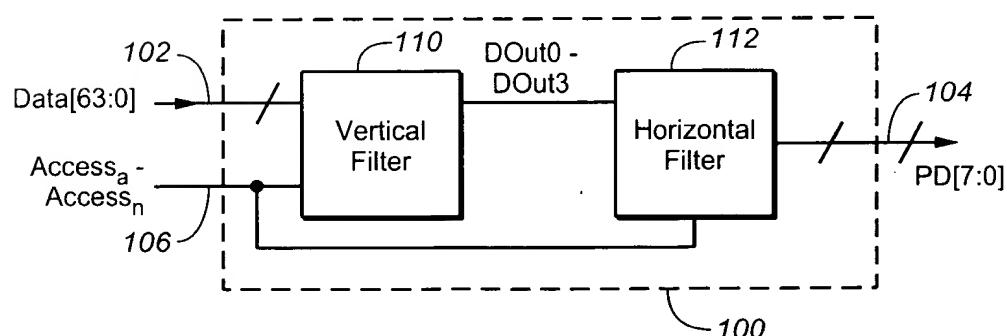
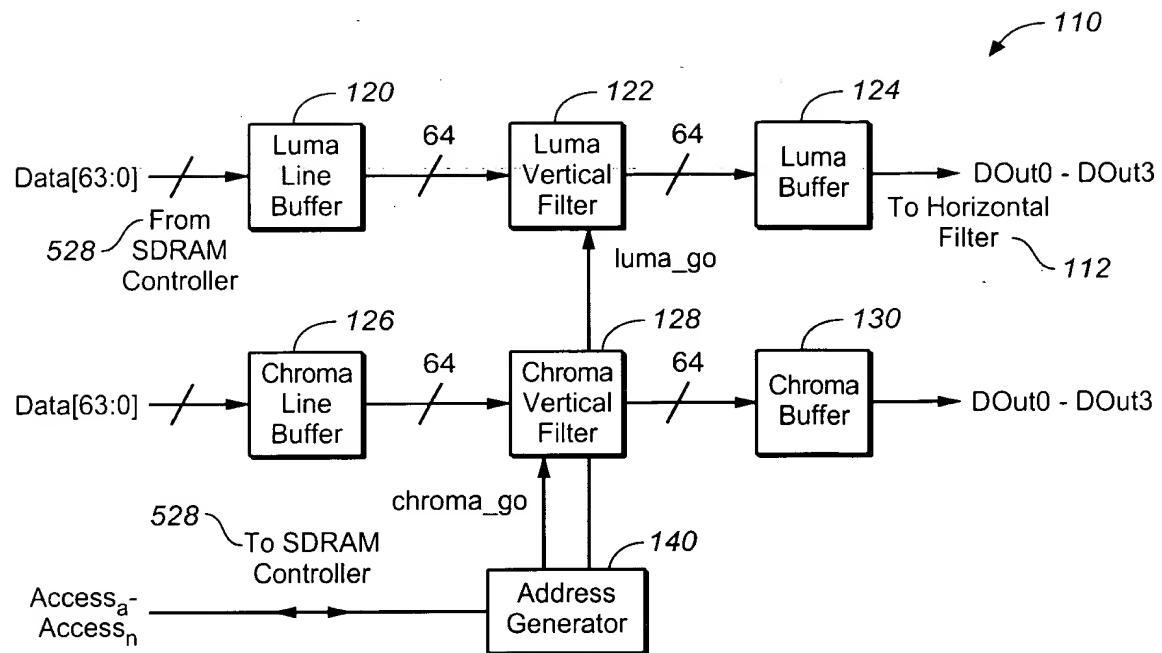
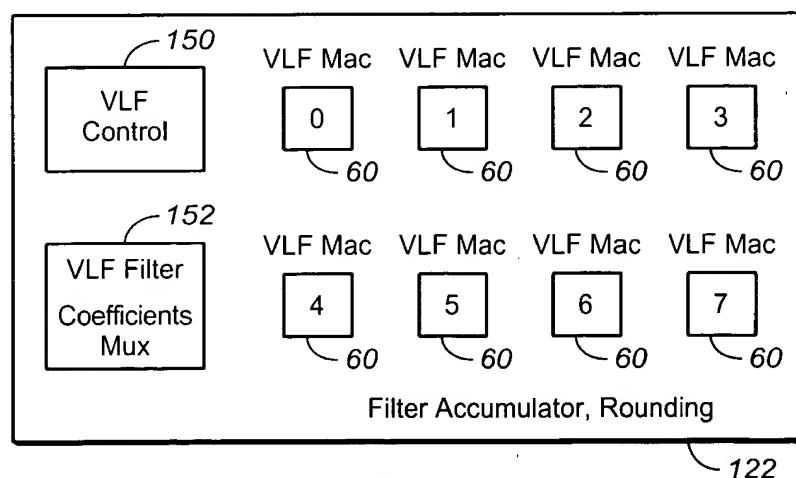
**FIG. 1****FIG. 2**

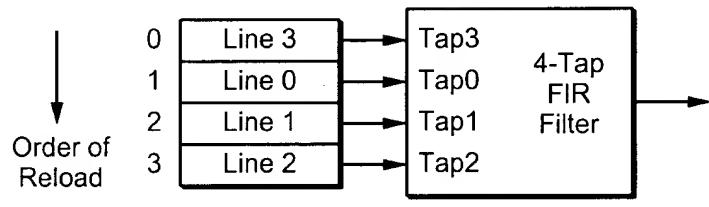
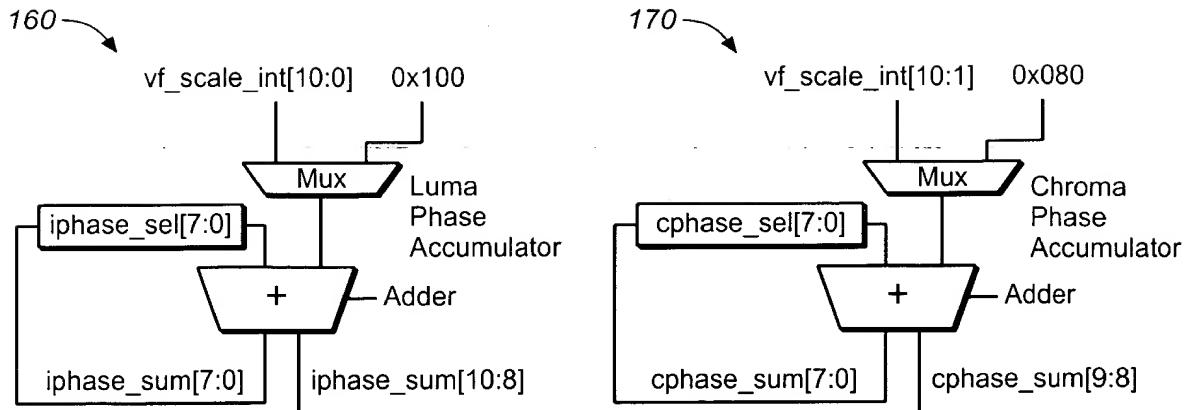
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**FIG. 3****FIG. 4**

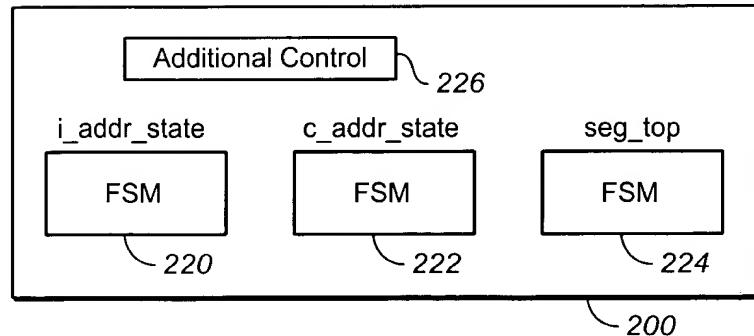
70

**FIG. 5****FIG. 6**

**FIG. 7****FIG. 8**



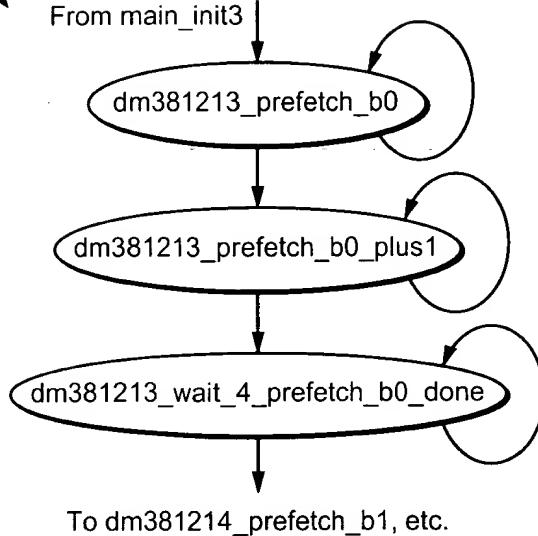
180 ↗



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300

From main_init3

FIG. 12

320

From Linstore Preload
dm381213_wait_4_prefetch_b3_doneTo End
of Field

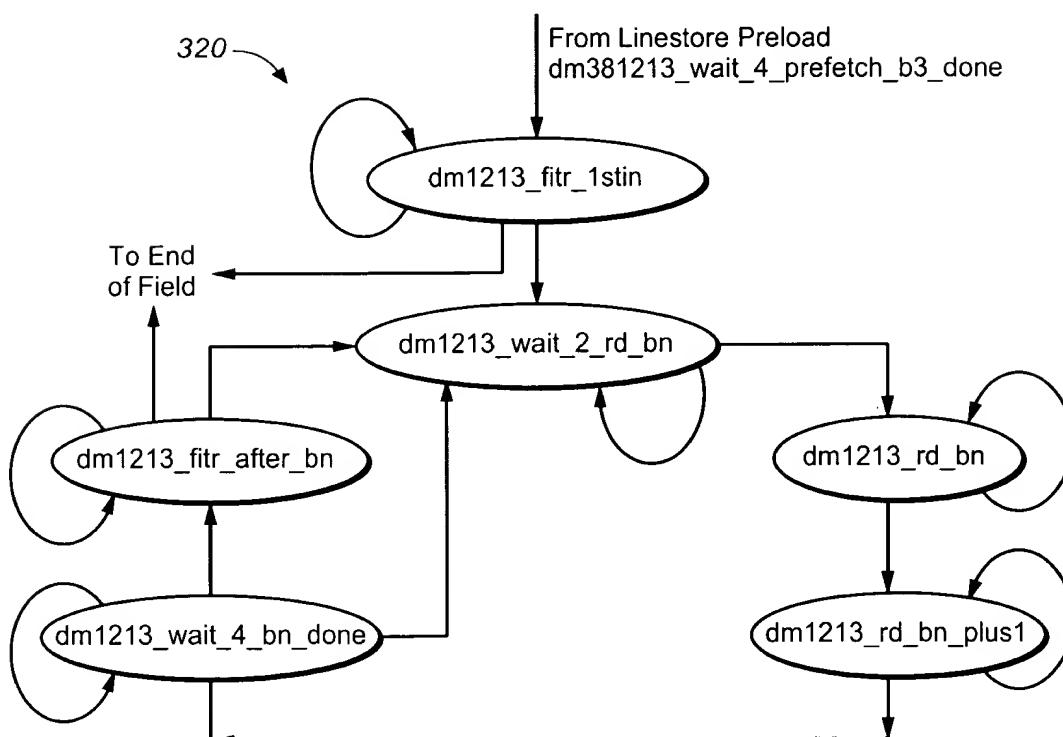
dm1213_fitr_1stin

dm1213_rd_bn

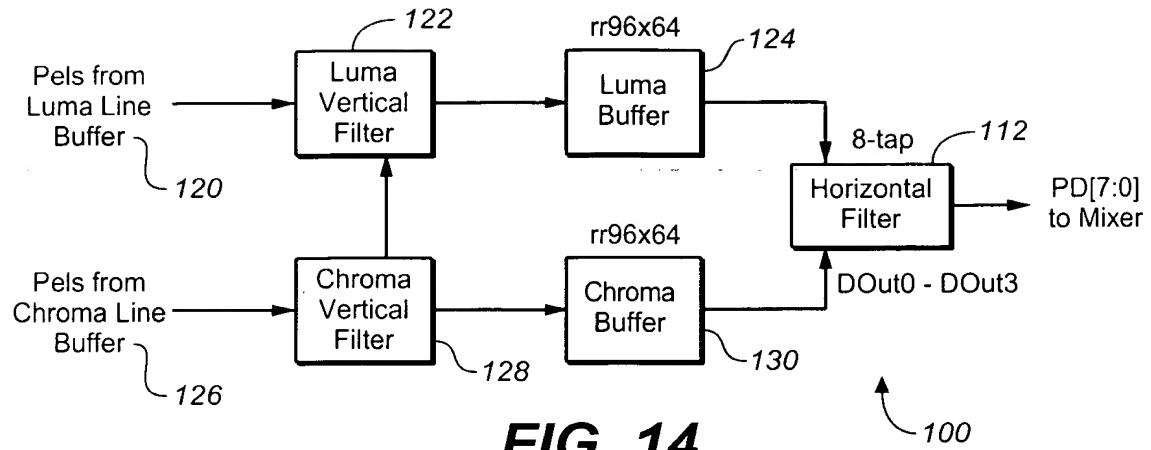
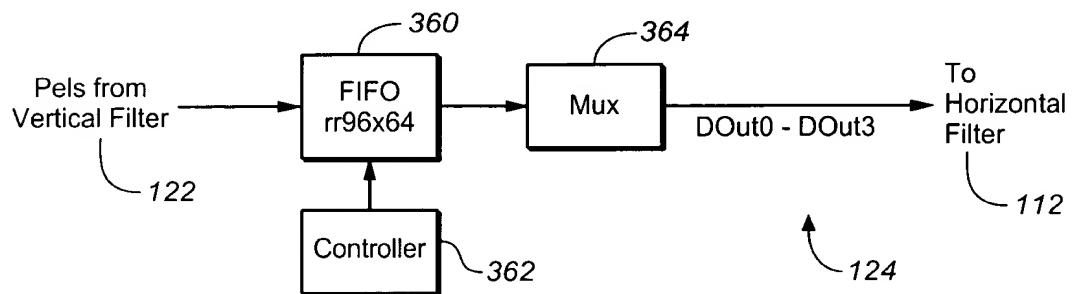
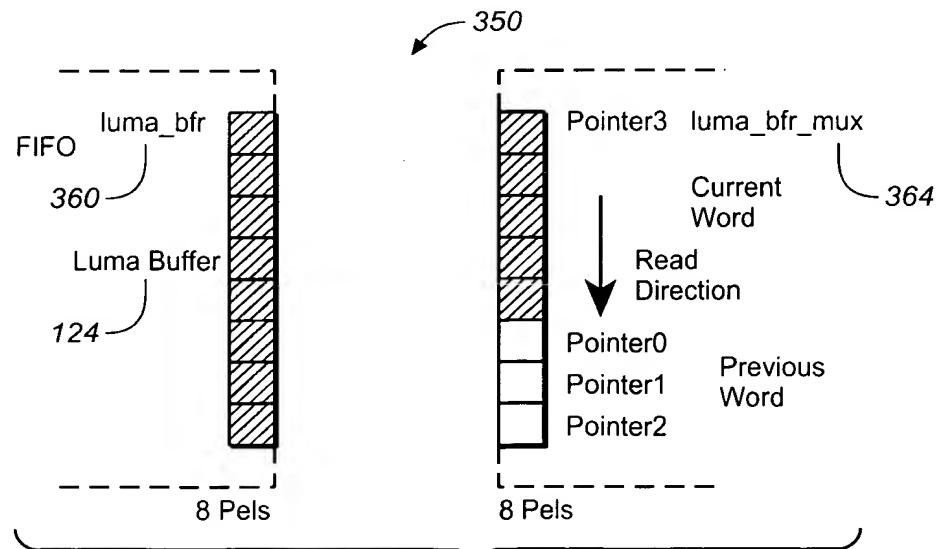
dm1213_rd_bn_plus1

dm1213_fitr_after_bn

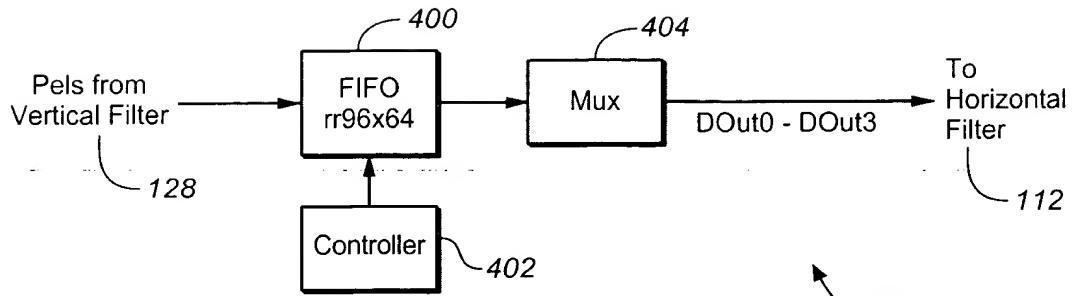
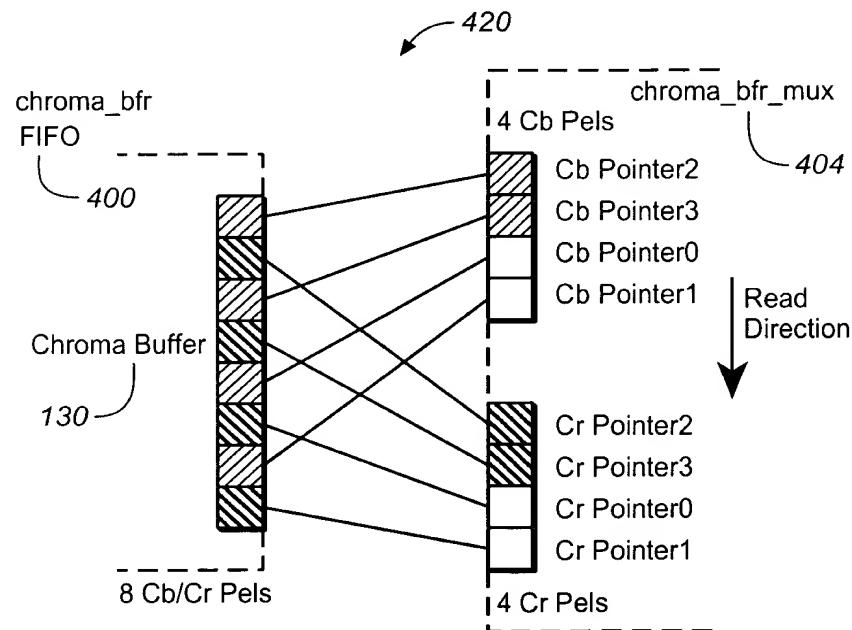
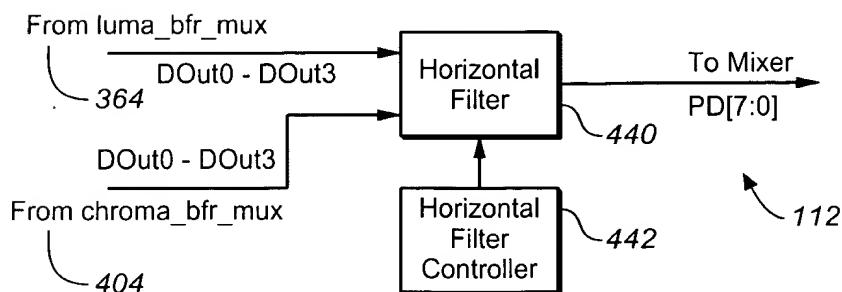
dm1213_wait_4.bn_done

FIG. 13

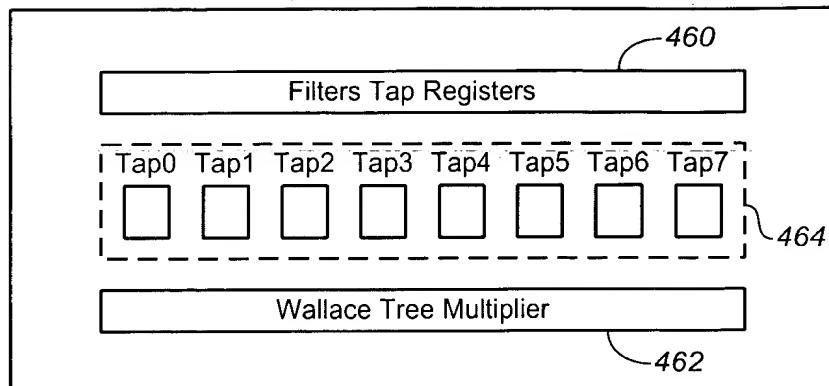
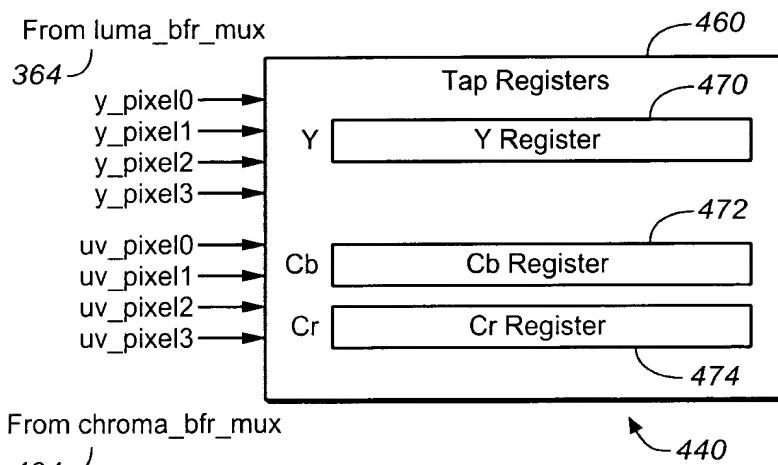
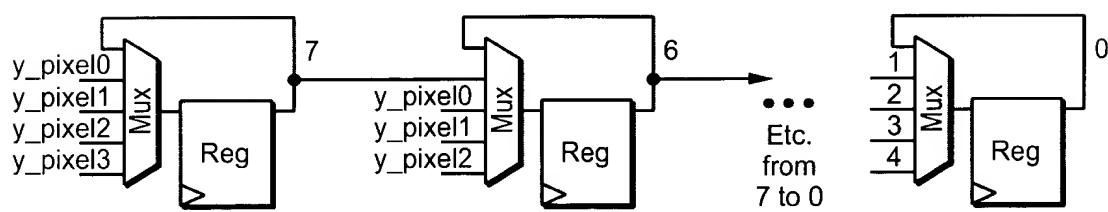
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**FIG. 14****FIG. 15****FIG. 16**

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**FIG. 17****FIG. 18****FIG. 19**

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**FIG. 20****FIG. 21****FIG. 22**

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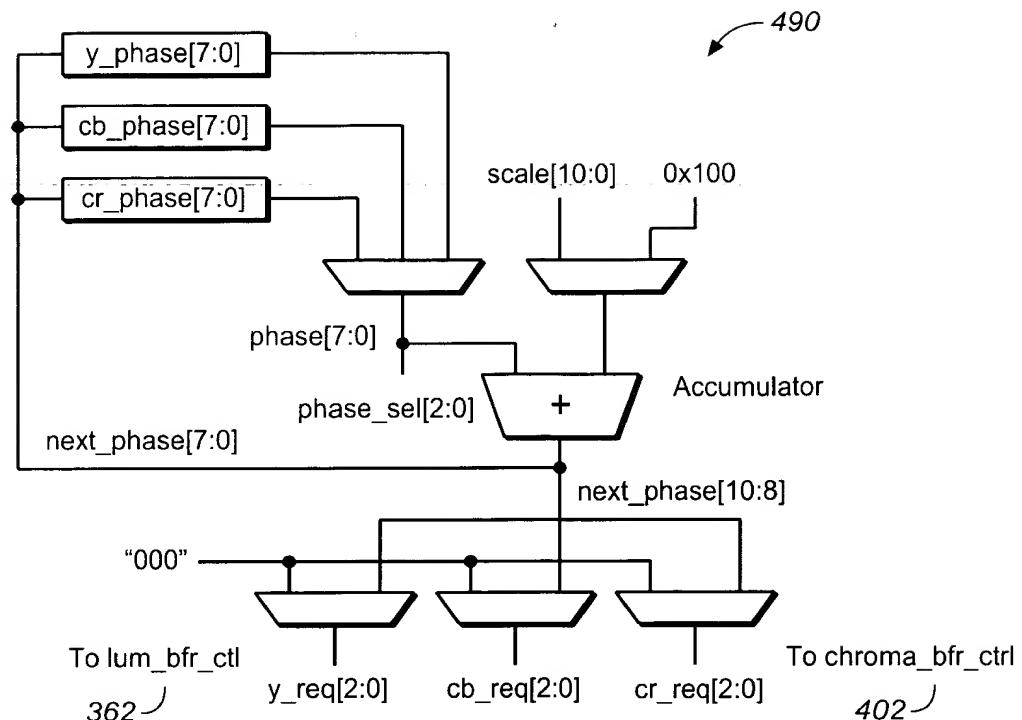


FIG. 23

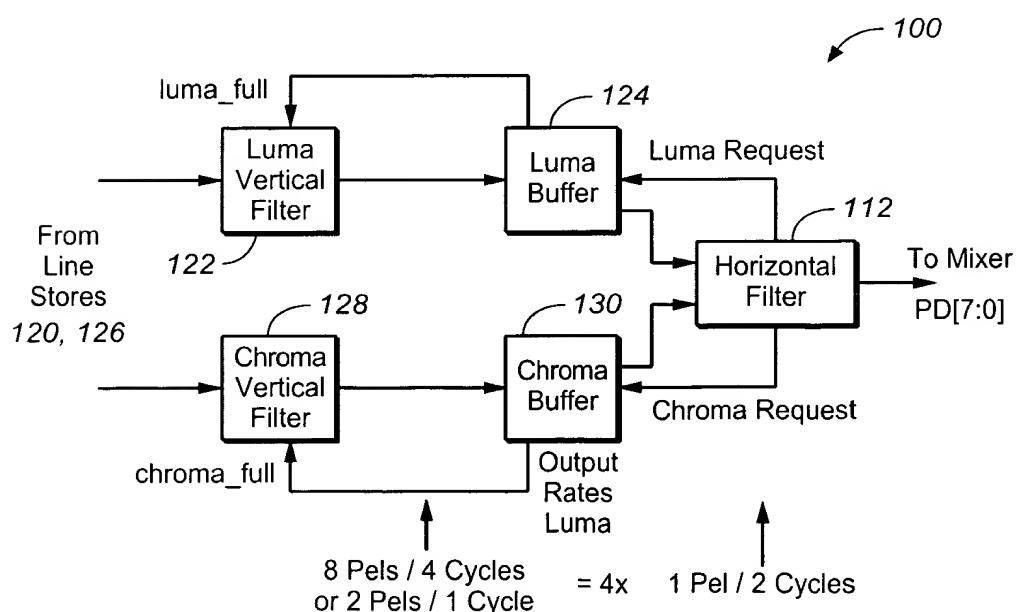
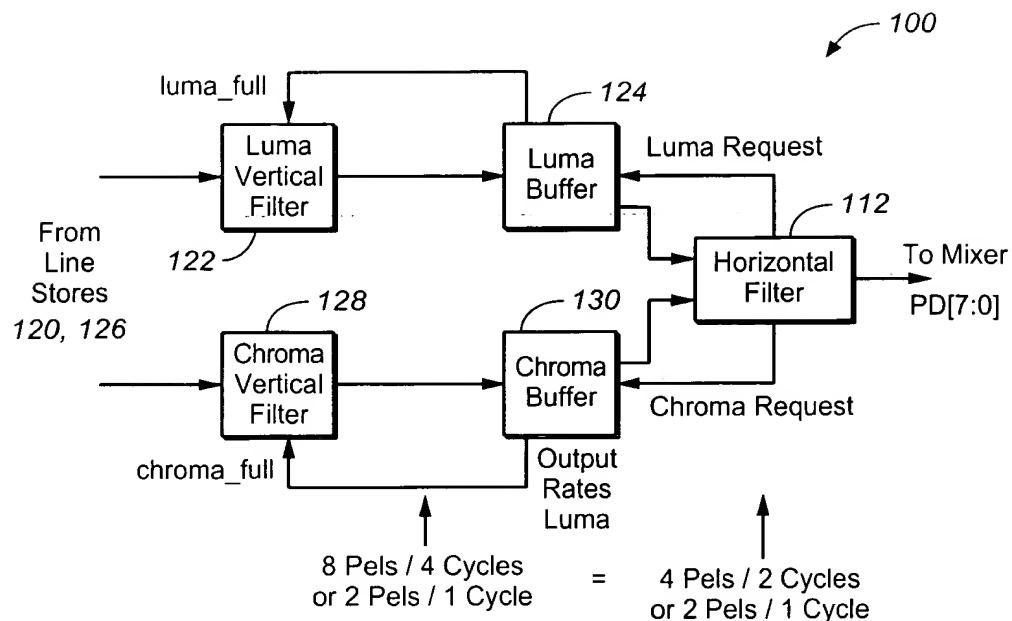
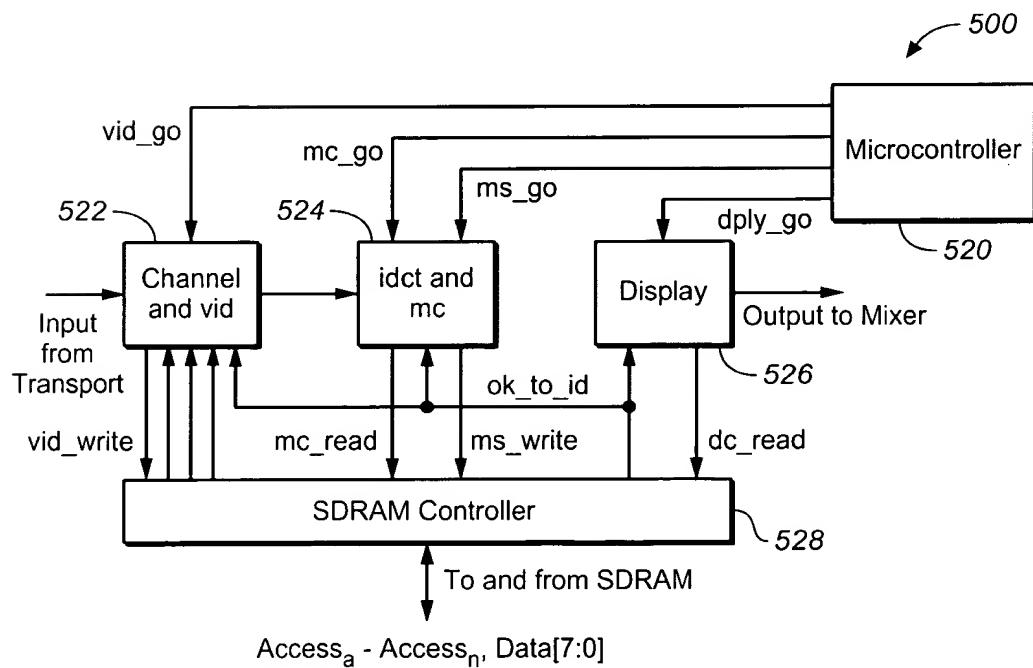
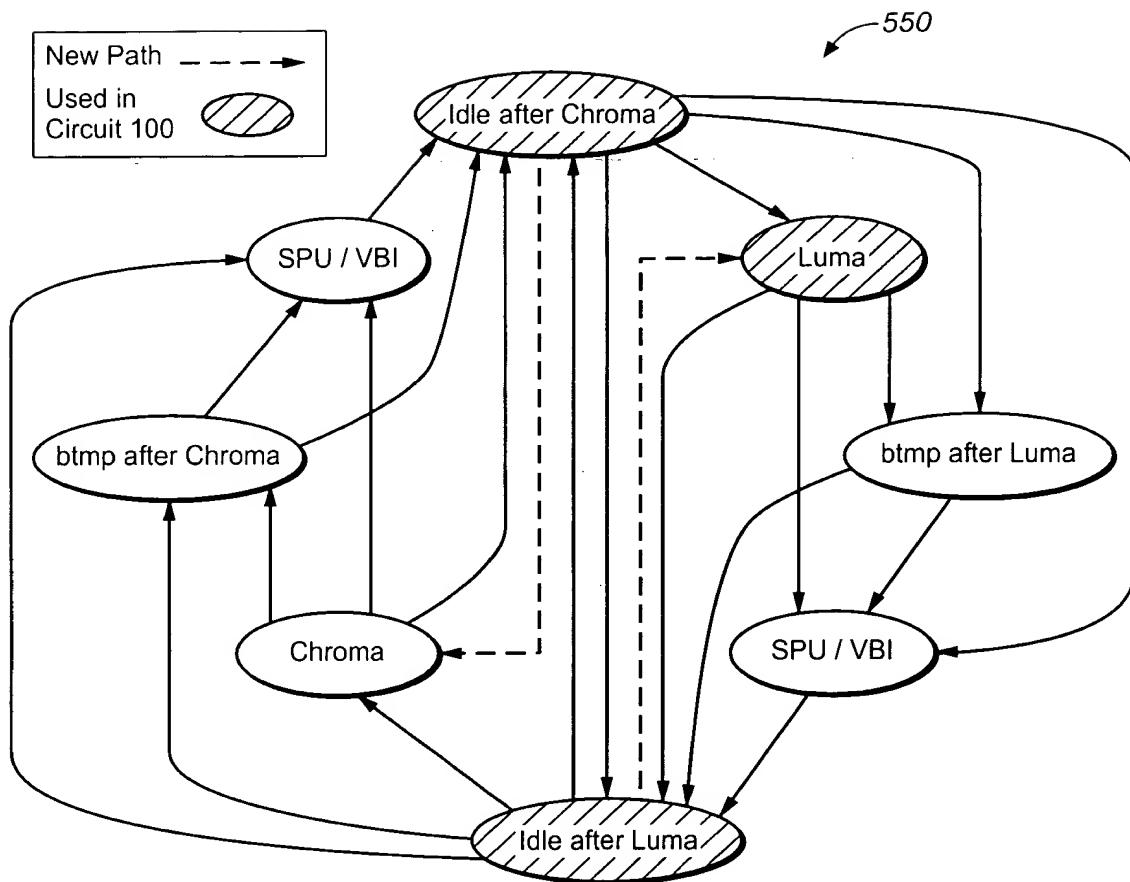
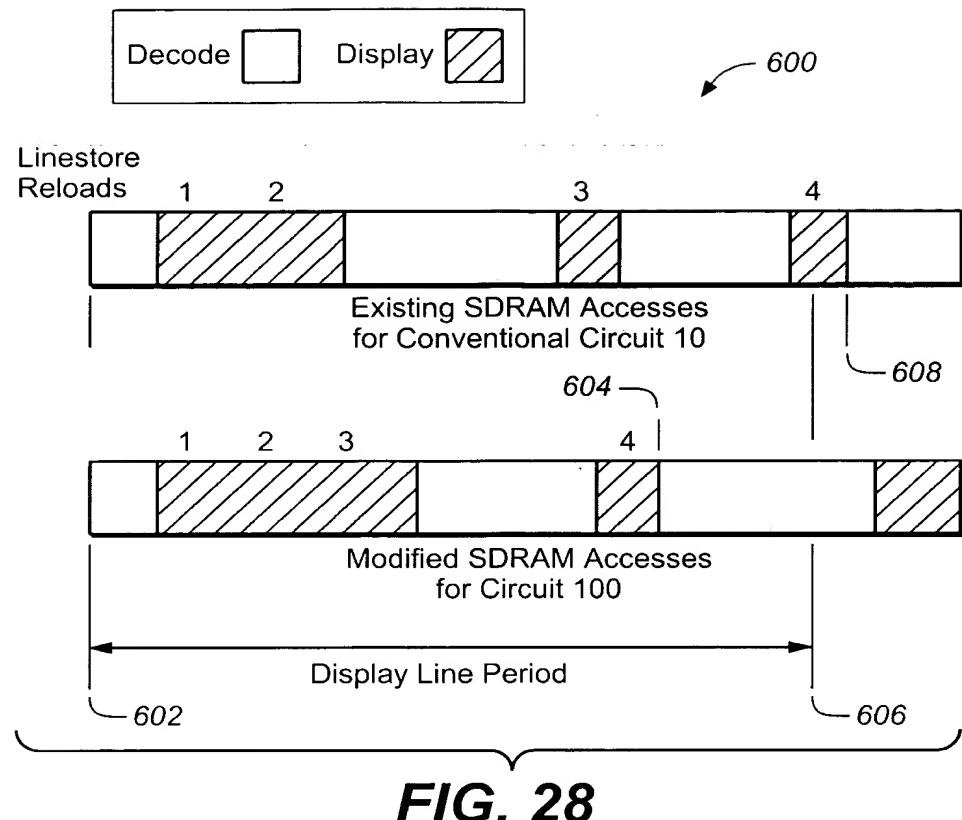
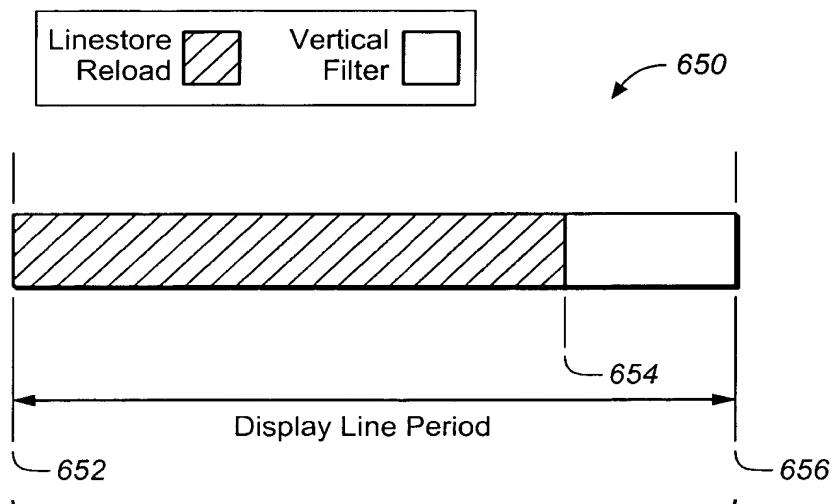


FIG. 24

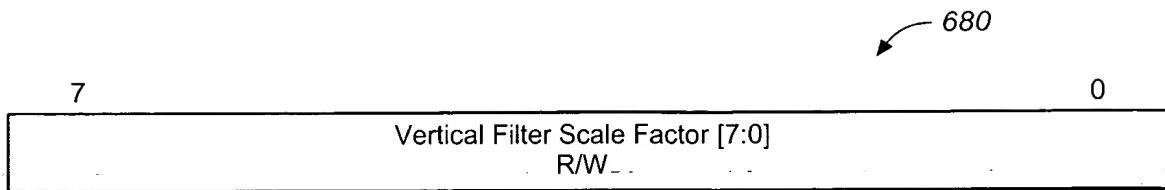
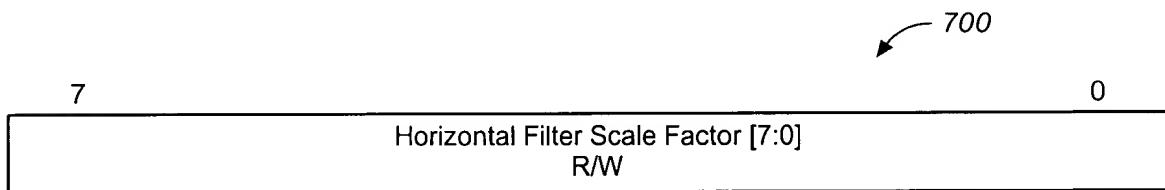
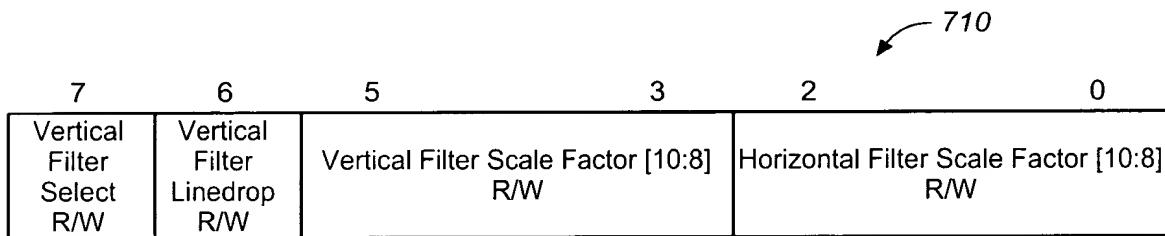
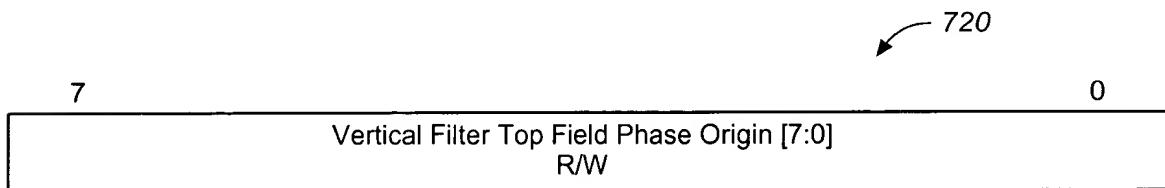
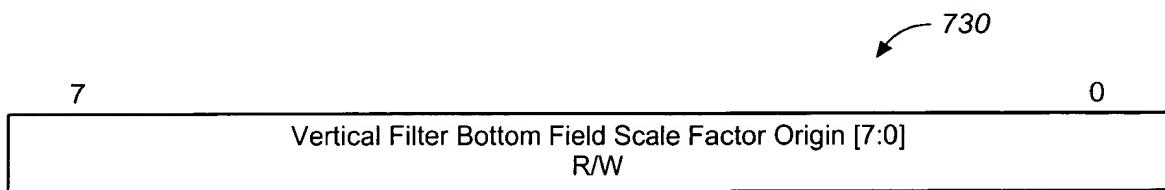
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**FIG. 25****FIG. 26**

**FIG. 27**

**FIG. 28****FIG. 29**

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**FIG. 30a****FIG. 30b****FIG. 31****FIG. 32a****FIG. 32b**